

**IN THE CLAIMS**

1. – 4. (Canceled).

5. (Previously Presented) A method for direct current (DC) feed control for a line card capable of being coupled to a subscriber line, comprising:

determining if the line card is operating in a current limit region of a direct current (DC) feed curve;

synthesizing a curve in the current limit region of the DC feed curve, wherein the synthesized curve is a linear curve with a negative slope;

determining a loop voltage based on the synthesized curve; and

applying the loop voltage to the subscriber line.

6. (Original) The method of claim 5, wherein determining if the line card is operating in the current limit region includes:

generating a current value proportional to a loop current flowing from the subscriber line;  
and

determining if the line card is operating in the current limit region of the DC feed curve in response to generating the current value.

7. (Original) The method of claim 6, wherein determining if the line card is operating in the current limit region of the DC feed curve in response to generating the current value includes determining if the loop current is greater than a first preselected value.

8. (Original) The method of claim 5, further including determining the loop voltage in at least one of an anti-saturation region and a resistance feed region in response to determining the line card is not operating in the current limit region.

9. (Previously Presented) A method for direct current (DC) feed control for a line card, comprising:

generating a current value proportional to a loop current flowing from a subscriber line;

determining if the line card is operating in a current limit region of a direct current (DC) feed curve in response to generating the current value; determining a loop voltage based on synthesizing a curve in the current limit region, wherein the curve is a linear curve with a negative slope; and applying the loop voltage to the subscriber line.

10. (Original) The method of claim 9, wherein determining if the line card is operating in the current limit region of the DC feed curve in response to generating the current value includes determining if the loop current is greater than a first preselected value.

11. (Original) The method of claim 9, further including determining the loop voltage in at least one of an anti-saturation region and a resistance feed region in response to determining the line card is not operating in the current limit region.

12. (Previously Presented) An apparatus, comprising:  
a digital signal processor for:  
determining if a line card is operating in a current limit region of a direct current (DC) feed curve; and  
determining a loop voltage based on a synthesized curve in the current limit region, wherein the curve is a linear curve with a negative slope; and  
a circuitry for applying the loop voltage to a subscriber line.

13. (Previously Presented) The apparatus of claim 12, wherein the digital signal processor for determining if the line card is operating in the current limit region includes the digital signal processor for:

generating a current value proportional to a loop current flowing from the subscriber line;  
and  
determining if the line card is operating in the current limit region of the DC feed curve in response to generating the current value.

14. (Original) The apparatus of claim 13, wherein the synthesized curve is based on an anti-saturation region and the current limit region of the DC feed curve.

15. (Previously Presented) The apparatus of claim 13, wherein the digital signal processor is further for determining the loop voltage in at least one of an anti-saturation region and a resistance feed region in response to determining the line card is not operating in the current limit region.

16. - 19. (Cancelled).

20. (Previously Presented) A line card, comprising:

a digital signal processor for:

determining if a line card is operating in a current limit region of a direct current (DC) feed curve; and

determining a loop voltage based on a synthesized curve in the current limit region, wherein the curve is a linear curve with a negative slope; and

a subscriber line interface circuit for applying the loop voltage to the subscriber line.

21. (Previously Presented) The line card of claim 20, wherein the digital signal processor is further for determining the loop voltage in at least one of an anti-saturation region and a resistance feed region in response to determining the line card is not operating in the current limit region.

22. (Original) The line card of claim 20, wherein the subscriber line interface circuit is a voltage-feed subscriber line interface circuit.

23. (Previously Presented) An apparatus, comprising:

means for determining if a line card adapted to be coupled to a subscriber line is operating in a current limit region of a direct current (DC) feed curve;

means for determining if the line card is operating in a current limit region of a direct current (DC) feed curve in response to generating a current value;

means for determining a loop voltage based on synthesizing a curve in the current limit region, wherein the curve is a linear curve with a negative slope; and means for applying the loop voltage to the subscriber line.

24. – 25 (Cancelled).

26. (Previously Presented) The method of claim 5, further comprising:  
receiving, using a first path, a signal and determining a cancellation current proportional to a current flowing from the subscriber line; and  
adjusting, using a second path, a DC level control based on the determined cancellation current, wherein adjusting a DC level control comprises providing a voltage to the subscriber line based on the cancellation current.

27. (Previously Presented) The method of claim 26, wherein the first path comprises an integrator for integrating the signal.

28. (Previously Presented) The apparatus of claim 12, further comprising:  
a first path for receiving a signal and determining a cancellation current proportional to a current flowing from the subscriber line; and  
a second path for adjusting a DC level control based on the determined cancellation current, wherein adjusting the DC level control comprises providing a voltage to the subscriber line based on the cancellation current.

29. (Currently Amended) The apparatus of claim 28, wherein the first path comprises an integratedintegrator for integrating the signal.

30. (Previously Presented) The apparatus of claim 29, wherein the apparatus operates in at least one of an anti-saturation region, a resistive feed region, and a current limit region, wherein the first path comprises a current limiter for limiting the digital signal to an upper limit of the current limit region.